

**S/N 09/537,274**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Larry Eugene Mosley  
Serial No.: 09/537,274  
Filed: March 29, 2000  
Title: MULTI-LAYER CHIP CAPACITOR

Examiner: Eric W. Thomas  
Group Art Unit: 2831  
Docket: 884.240US1

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JAN 21 2003  
PATENT & TRADEMARK OFFICE  
Commissioner for Patents  
Washington, D.C. 20231

#14/177-03  
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**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

The Applicant's representative has reviewed the Office Action mailed on October 24, 2002. Please amend the above-identified patent application as follows, and consider the appended remarks.

**IN THE CLAIMS**

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect cancellation of claims 1 and 8, as well as the amendment of previously pending claims 2, 4, 6, and 7. The specific amendments to individual claims are detailed in the following marked up set of claims.

Please cancel claims 1 and 8.

Please amend the following claims:

2. (Amended) A multi layer integrated circuit capacitor comprising:  
a substrate;  
a first conductive layer located over and contacting the substrate;  
a first insulator layer located over and contacting the first conductive layer, the first insulator layer not contacting the substrate;  
a second conductive layer located over the first insulator layer;  
a second insulator layer located over the second conductive layer;  
a third conductive layer located over the second insulator layer;  
a third insulator layer located over the third conductor layer;